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**Exemplar Logic Synthesis Integration with Cypress *Warp*<sup>™</sup> Software  
Adds Development Flexibility for Delta39K CPLDs**

*Seamless Integration Enables Designers  
to Work in Either LeonardoSpectrum or Warp Environment*

**SAN JOSE, Calif., Aug. 30, 2000** — Exemplar Logic Inc.<sup>®</sup>, a Mentor Graphics Company, today announced LeonardoSpectrum<sup>™</sup> synthesis tool integration with Cypress Semiconductor's *Warp*<sup>™</sup> Release 6.0 complex programmable logic device (CPLD) development environment. The integration provides VHDL or Verilog designers with the option of working in either the LeonardoSpectrum or Warp development environment to optimize their high-performance Delta39K designs. The new capabilities extend LeonardoSpectrum's support for Cypress CPLDs, which include Cypress's Ultra37000<sup>™</sup> CPLD family. As a supporter of Cypress CPLDs, Exemplar Logic joins the Cypress Federation, a coalition of third-party CPLD design tool providers endorsed by Cypress Semiconductor.

LeonardoSpectrum combines push-button ease-of-use with the powerful control and optimization features to offer CPLD designers the fastest, most efficient synthesis environment. LeonardoSpectrum takes in Verilog and VHDL code and outputs an optimized netlist that can be read directly into Cypress's *Warp* environment. In the *Warp* environment, a Delta39K design can be fitted and mapped for speed or area depending on the end-use application requirements.

“With the addition of Exemplar to the Cypress Federation of third-party tool providers, Cypress customers now have access to the market-leading programmable logic synthesis tool for device optimization,” said Rich Kapusta, product marketing manager, programmable logic group, Cypress Semiconductor. “As part of our commitment to providing flexibility to designers, we aim to partner with more software tool vendors to create a seamless integration between third-party tools and the *Warp* software suite. The end goal is to allow designers to target Cypress CPLDs with a tool they are comfortable using while taking advantage of *Warp*’s advanced fitting algorithms and analysis tools.”

As part of the synthesis process LeonardoSpectrum replaces arithmetic, relational and memory operators with models optimized for Cypress CPLDs. Cypress’s *Warp* software then builds the most efficient circuit using advanced fitting algorithms and user preferences.

“LeonardoSpectrum is the perfect complement to Cypress’s language-independent *Warp* programmable logic development environment,” added Tom Feist, Exemplar Logic’s vice president of marketing. “We are committed to providing the synthesis component for maximizing Delta 39K performance while minimizing development time and costs.”

### **Price and Availability**

Customers will require a copy of LeonardoSpectrum 2000.1a2 or b, available at <http://www.exemplar.com>, and a copy of Cypress’s *Warp* software, which may be downloaded from <http://www.cypress.com/pld/warp.html>. More information may be accessed at <http://www.cypress.com/pld/exemplar>. LeonardoSpectrum 2000 runs on Windows®-NT, 95, 98 and 2000, Solaris and HP-UX platforms. LeonardoSpectrum is also available as part of the FPGA Advantage™ Design Solution from Mentor Graphics.

### **About Cypress Semiconductor Corp.**

Cypress Semiconductor provides high-performance integrated circuit solutions “By Engineers. For Engineers.™” for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial control. With a focus on emerging communications applications, Cypress’s product lines include networking-optimized and micropower static RAMs; high-bandwidth multi-port and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital

systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

Cypress's shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

"Safe Harbor" Statement under the Private Securities Litigation Reform Act of 1995: Statements herein that are not historical facts are "forward-looking statements" involving risks and uncertainties. Please refer to Cypress's Securities and Exchange Commission filings for a discussion of such risks.

### **About Exemplar Logic, Inc.**

Exemplar Logic, Inc. is a Mentor Graphics Company (NASDAQ: MENT). Exemplar pioneered applying logic synthesis techniques to the design of FPGAs, CPLDs and ASICs. It is the world's number one CPLD and FPGA synthesis tool supplier, as reported by Dataquest. Exemplar's products are sold and supported worldwide by Exemplar Logic, its OEMs and VARs, Mentor Graphics and through the FPGA and CPLD component distribution channel. Exemplar's design environments implement a complete high-level design solution for FPGA, CPLD and ASIC design, offering synthesis, simulation and timing analysis for Windows® 95/98, Windows-NT and UNIX (HP, Sun) platforms in server and non-server environments. Exemplar Logic is located at 880 Ridder Park Drive, San Jose, California 95131. For information on Exemplar representatives or products, please call (408) 487-7000, e-mail: [sales@exemplar.com](mailto:sales@exemplar.com), or visit [www.exemplar.com](http://www.exemplar.com).

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